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also known as complementary-  
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metal-oxide-semiconductor,  
is a type of  
metal-oxide-semiconductor  
field-effect transistor  
fabrication process that  
uses complementary and  
symmetrical pairs of p-type  
and n-type MOSFETs for logic  
functions. CMOS technology  
is used for constructing  
integrated circuit chips,  
including microprocessors,  
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